

SEIKO EPSON CORPORATION

CRYSTAL OSCILLATOR (SPXO) OUTPUT : CMOS



Product Number (please contact us) SG2016CAN: X1G004801xxxx00 SG-210STF: X1G004171xxxx00 SG3225CAN: X1G005961xxxx15 SG5032CAN: X1G004451xxxx00 SG7050CAN: X1G004481xxxx00

SG2016 / 3225 / 5032 / 7050CAN SG-210STF

- Frequency
- Supply voltage
- Function
- : 1.8 V to 3.3 V Typ. : Standby(ST)

20 standard frequencies

• Operating temperature :

:

- -40 °C to +105 °C





(3.2 x 2.5 mm)





(2.5 x 2.0 mm) (2.0 x 1.6 mm)

SG5032CAN (5.0 x 3.2 mm)

Specifications (characteristics)

Item	Symbol		Sn	ecifications	3				Condi	tions / Remar	ks	
nom	Cymbol						12.288 MHz		Condi		10	
Output frequency	fo	14.7456 MHz 16 25 MHz 26	MHz MHz MHz	20 MHz 27 MHz 48 MHz	24 MH 32 MH 50 MH	⊣z ⊣z	24.576 MHz 33.33 MHz 72 MHz					
			1.60	0 V to 3.63 V	/			4 MHz \leq	fo \leq 50 MHz, T	_use = +105 °C		
Supply voltage	Vcc	1.71 V to 3.63 V					$f_0 = 72 \text{ MHz}$ T uso = $\pm 85 ^{\circ}\text{C}$ Mox		Refer t			
		2.25 V to 3.63 V				fo = 72 N	1Hz, T_use = +	105 °C Max.		, ,		
Ot	T -4-		-55 °	°C to +125 °	С			SG20160	CAN, SG32250	CAN		
Storage temperature	T_stg	-40 °C to +125 °C				All others	3					
Operating temperature	T_use	-20 °C to +70 °	°C, -40	°C to +85 °C	c, -40 °C	C to +	105 °C	See of fig	gure *1			
Fraguenes telerence	f tol		ť	±25 × 10 ⁻⁶				-20 °C to +70 °C				
Frequency tolerance	f_tol		ť	±50 × 10 ⁻⁶				-40 °C to	+85 °C, -40 °C	C to +105 °C		
		V _{CC} = 1.8 V ± 10 %	V _{CC} =	= 2.5 V ± 10	% V	/ _{cc} = 3	3.3 V ± 10 %					
	lcc	1.5 mA Max.	1.	6 mA Max.		1.8	mA Max.	No load condition, 4 MHz \leq fo \leq 20 MHz				
Current consumption		1.8 mA Max.	2.	0 mA Max.		2.2	mA Max.	No load condition, 20 MHz < fo \leq 40 MHz			Ηz	
		2.1 mA Max.	2.	4 mA Max.		2.6	mA Max.	No load condition, 40 MHz < fo \leq 50 MHz				
		2.4 mA Max.	2.	8 mA Max.		3.0	mA Max.	No load condition, fo = 72 MHz				
Stand-by current	I_std	2.1 μA Max. 2.5 μA Max. 2.7 μA Max.			ST =GN	D						
Symmetry	SYM	45 % to 55 %						50 % Vcc	e level, L_CMO	S ≤ 15 pF		
	V _{OH}	90 % V _{CC} Min.					1.8 V ± 10 % -1.5 mA	2.5 V ± 10 %	3.3 V ± 10 %]		
Outer the set	Vol	10 % V _{CC} Max.					loн lol	1.5 mA	-3 mA 3 mA	-4 mA 4 mA	_	
Output voltage	V _{OH-2}	V _{CC} - 0.4 V Min.						1.8 V±10 %	2.5 V±10 %	3.3 V±10 %]	
	V _{OL-2}	0.4 V Max.					loн lol	-3 mA 3 mA	-4 mA 4 mA	-6 mA 6 mA	-	
Output load condition (CMOS)	L_CMOS		15 pF Max.									
Innutvaltaga	VIH	80 % V _{CC} Min.					ST terminal					
Input voltage	VIL		20	% V _{CC} Max				- ST terminal				
Rise time and Fall time	tr / tf	3 ns Max. 3.5 ns Max. (@1.8 V±10 %)				20 % V_{CC} to 80 % V_{CC} level, L_CMOS = 15 pF						
Start-up time	t_str	3 ms Max.					T = 0 at 90 % V _{CC}					
Frequency aging	f_age	±3 × 10 ⁻⁶ / year Max.				+25 °C, First year						

[Model: SG2016/3225/5032/7050CAN]

SG2016 C AN 25.00000MHz T J H A Product name (Standard form) 3 (1)(4)(5)(6)(7)(2)

①Model ②Output(C: CMOS) ③Frequency ④Supply voltage

⑤Frequency tolerance ⑥Operating temperature range

⑦Internal identification	code("A"	is default)
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0			/			
④Su	pply voltage *See Figure 1	5Fre	⑤Frequency tolerance / ⑥Operating temperature range			
Т	1.8 V to 3.3 V Typ.	DB*	±25 × 10 ⁻⁶ / -20 °C to +70 °C			
К	2.5 V to 3.3 V Typ.	JG	±50 × 10 ⁻⁶ / -40 °C to +85 °C			
		JH	±50 × 10 ⁻⁶ / -40 °C to +105 °C			

* Please refer to Product number list on Full Data Sheet for available frequencies

[Model: SG-210STF]

T 1.8

[
Product name	SG	-210 S T	F 25.000	000MHz	Y
(Standard form)	1	23	4	5	

①Model ②Function(S:Standby) ③Supply voltage

④Frequency ⑤Frequency tolerance ③Supply

, , ,						
ly voltage *See Figure 1	5Fre	5 Frequency tolerance				
.8 V to 3.3 V Typ.	S*	±25 × 10 ⁻⁶ / -20 °C to +70 °C				
	L	±50 × 10 ⁻⁶ / -40 °C to +85 °C				
	Y	±50 × 10 ⁻⁶ / -40 °C to +105 °C				
* Please refer to Product number list on Full Data Sheet for available frequencies						

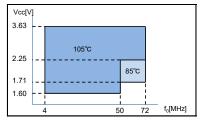
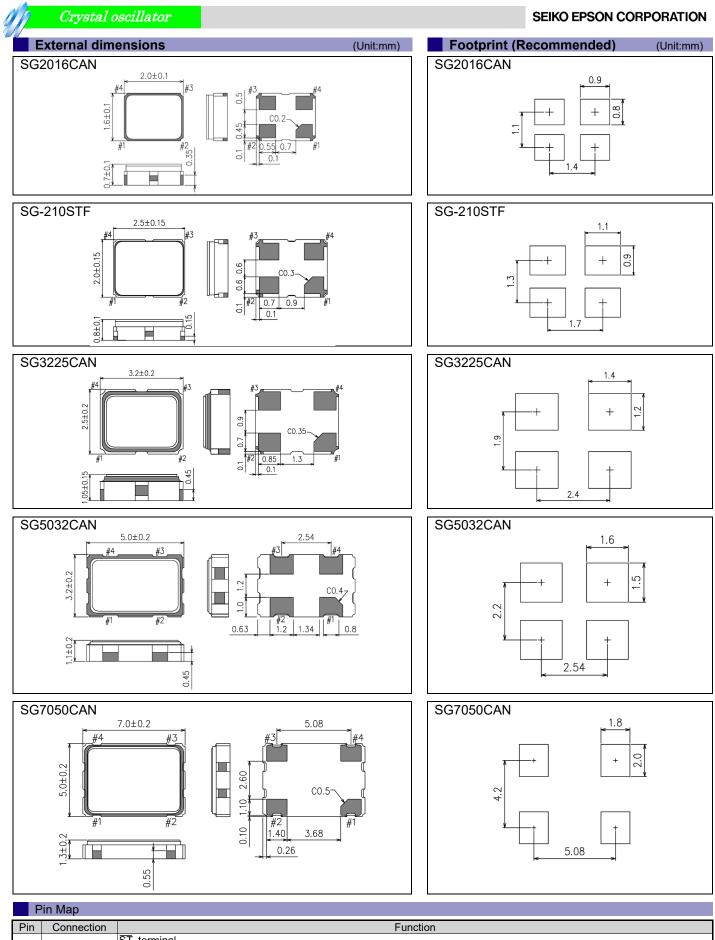


Figure 1 : The upper limit of Operating temperature and the related conditions

Please note that Supply voltage range (V_{CC}) depends on Output frequency (fo) and upper limit of Operationg temperature (T_use Max.).



Pin	Connection		Function					
		ST term	ninal					
1	ST		ST function	Oscillator circuit	Output			
	1 51		HIGH or "open"	Oscillation	Specified frequency: Enable			
			LOW	Oscillation stop	High impedance: Disable			
2	GND	Ground						
3	OUT	Clock ou	utput					
4	V _{cc}	Power s	upply					
Mate	To mediately	مغماما مع		O du⊑ hu nass semesiter et a lassti	ien een veen een veersiele te the verview een veerste termine			

Notes: To maintain stable operation, provide a 0.01uF to 0.1uF by-pass capacitor at a location as near as possible to the power source terminal of the crystal product (between Vcc - GND).

PROMOTION OF ENVIRONMENTAL MANAGEMENT SYSTEM CONFORMING TO INTERNATIONAL STANDARDS

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